memory 18 which is represented in figure 2 and comprises three connections even though it is only a dual-port memory or single-port memory. Several communications partners, in the present case therefore, the bypass unit 17, the bypass interface 19, and the coordination unit 22, can access, for example, via a common physical communications path, an additional unit, here therefore the bypass memory 18.

[0090] An additional embodiment of an adjustment device 1 permits providing the data received from the coordination unit 22, for example, data which are transmitted to the operating unit 7, with a time stamp. This is advantageous because the time information of the time stamp permits temporal indexing, for example, of measured data, as well as, for example, making possible a temporal synchronization of several adjustment devices 1 operated in one network.

[0091] An alternative embodiment of the adjustment device 1 already applies a time stamp to the data running through the programmable unit 5 so that this operational step no longer has to be carried out by the coordination unit 22. Also in this variant, temporal advantages vis-à-vis using the coordination unit 22 can be achieved.

[0092] The coordination unit 22 serves, for example, to interpret configuration instructions coming from the operating unit 7 and/or from the simulation unit 20 and to configure the adjustment device 1 accordingly. Thus, it is, for example, even possible to set up the adjustment device 1 according to the invention not only for a special type of control device debug interface 8 but rather to adapt it to practically any interface standards.

[0093] As represented in Figure 2, the coordination unit 22, for example, also comprises connections via which external trigger signals 24 as well as internal trigger signals 25 can be registered and evaluated, on account of which, with the coordination unit 22, it is possible in a simple manner to activate corresponding units 13, 14, 15, 17 of the programmable unit 5.

[0094] The coordination unit 22 is located embodiment according to Figure 2, by a separate computer unit outside of the programmable unit 5, for example, by a microcontroller. In an additional variant of the adjustment device 1, the coordination unit 22 is, on the contrary, formed by a programmable logic chip, or as a part of the programmable unit 5.